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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention

METHOD FOR CREATING A SELF-ALIGNED SOI DIODE BY
REMOVING A POLYSILICON GATE DURING PROCESSING

Application Number :

Date :

First Named Applicant: Kiran V. Chatty

Confirmation Number:

Attorney Docket Number: BUR920030180US1

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Documents being submitted:	Files
us-fee-sheet	BUR920030180US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-assignment	BUR920030180US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920030180Assign1.tif BUR920030180Assign2.tif
us-request	BUR920030180US1-usrequ.xml us-request.dtd us-request.xsl
application-body	BUR920030180-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920030180drw1.tif BUR920030180drw2.tif BUR920030180drw3.tif BUR920030180drw4.tif BUR920030180drw5.tif BUR920030180drw6.tif BUR920030180drw7.tif
us-declaration	BUR920030180Dec1.tif
us-declaration	BUR920030180Dec2.tif
Comments	

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